

Figure 1 Monoline Serial Interface

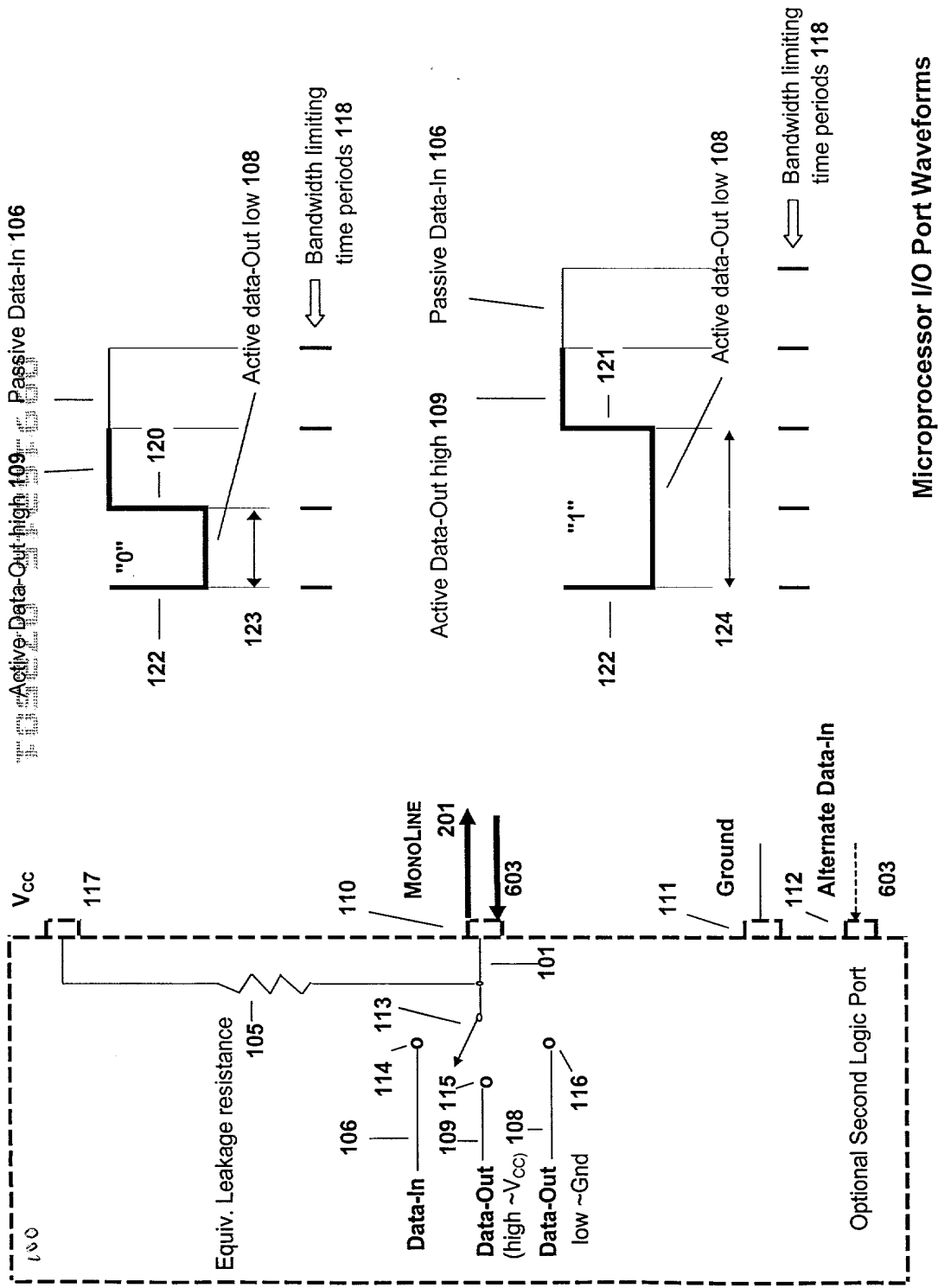
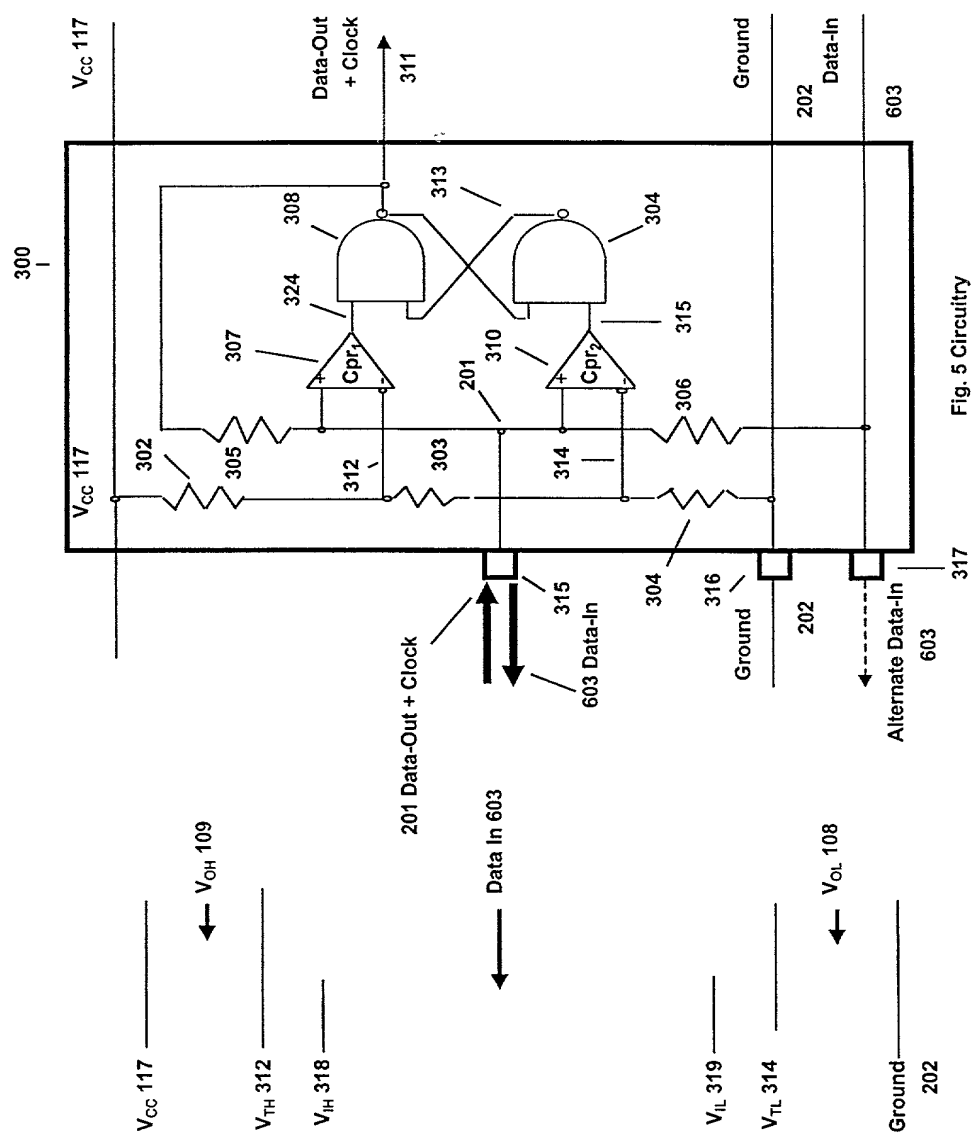


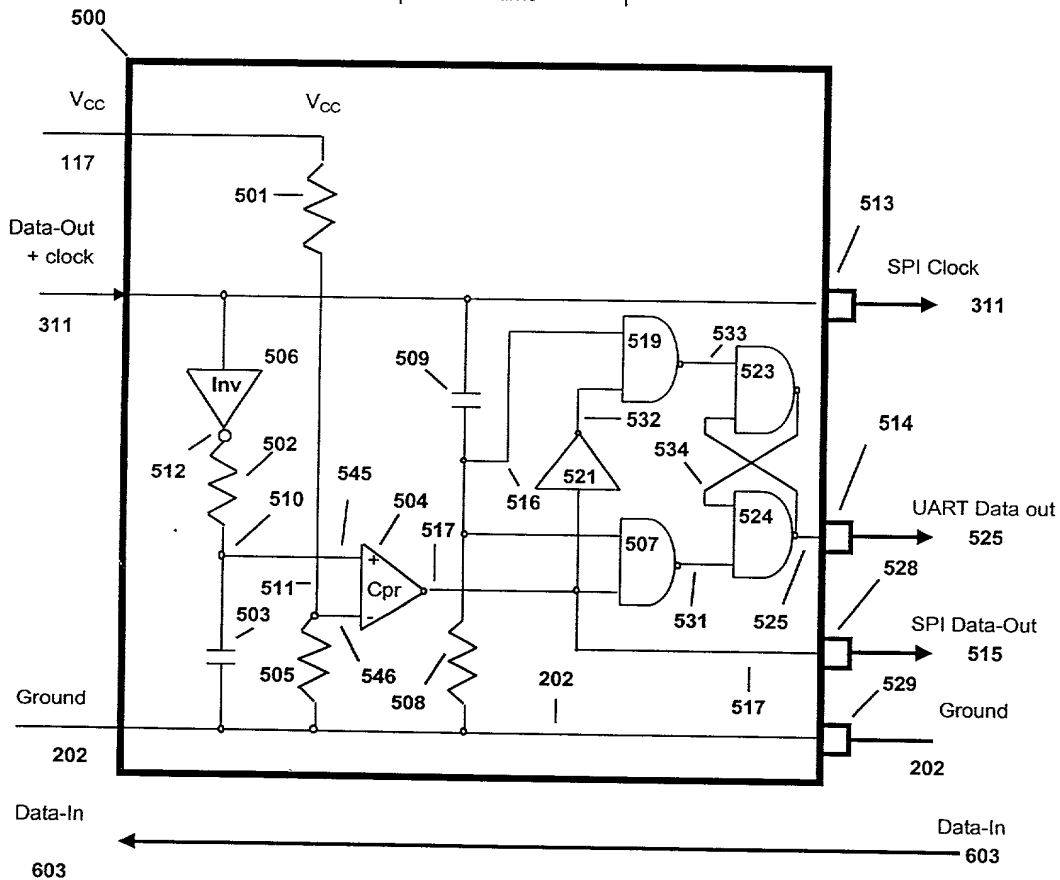
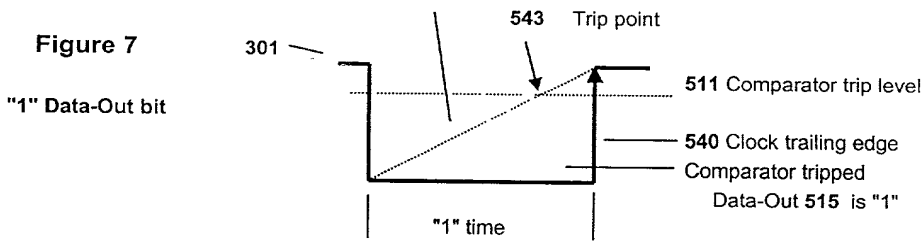
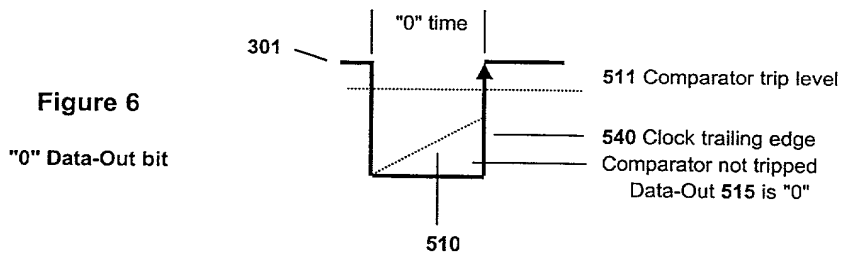
Figure 2

Figure 3

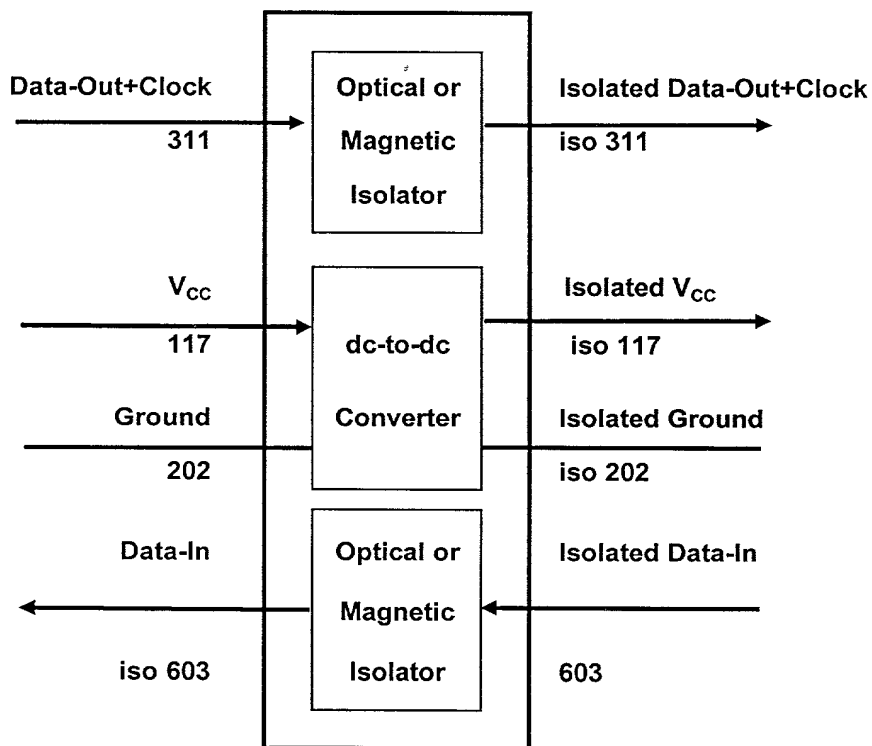
FIG. 4



Data Direction Multiplexer



Data-Out Extractor Circuit
Figure 8



Optional Electrical Isolation

Figure 9

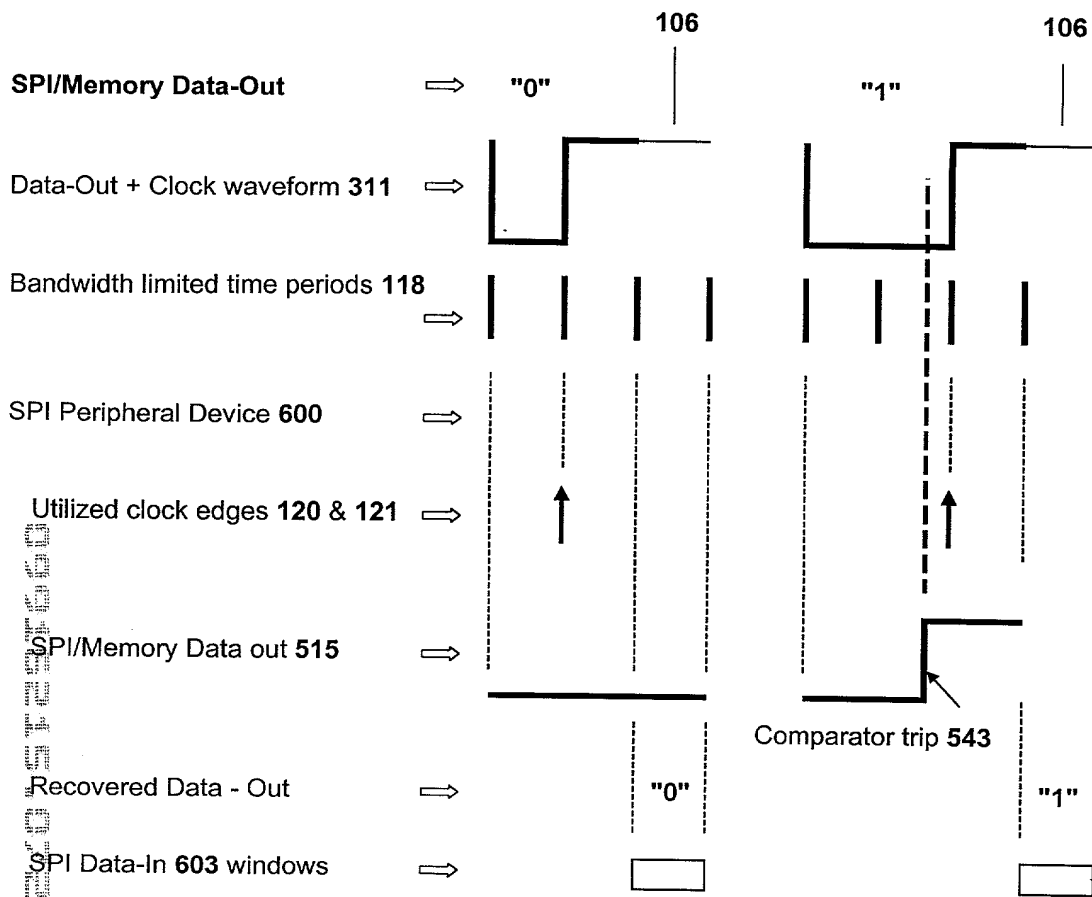


Figure 10 SPI Data

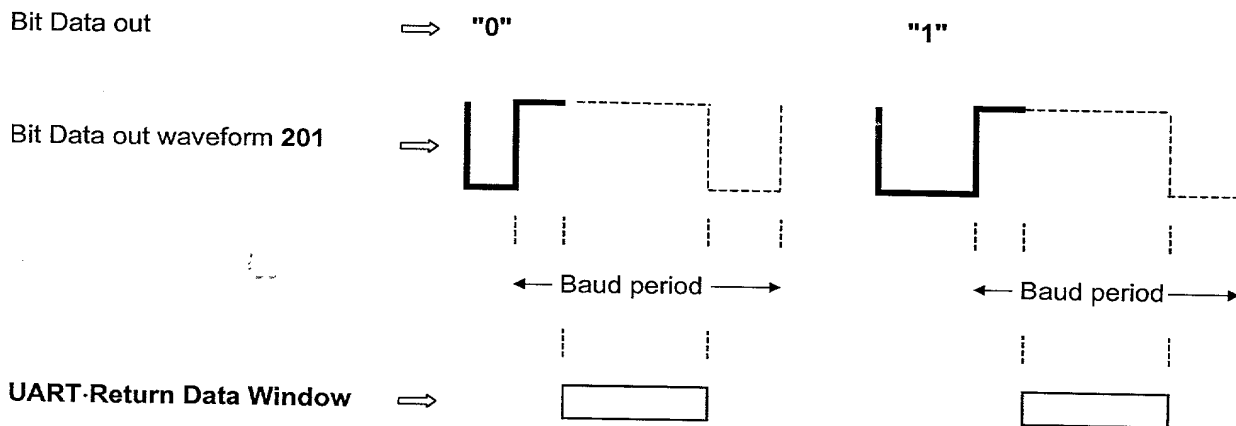


Figure 11 UART Data